



3FT1T3

主要参数 MAIN CHARACTERISTICS

$I_{T(RMS)}$	1.0A
V_{DRM}	600V/800V
I_{GT}	5mA

用途

- 交流开关
- 相位控制

产品特性

- 玻璃钝化芯片, 高可靠性和一致性
- 低通态电流和高浪涌电流能力
- 环保 RoHS 产品

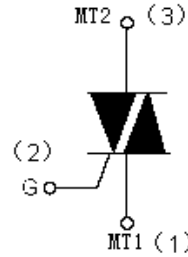
APPLICATIONS

- AC switching
- Phase control

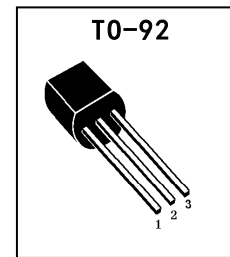
FEATURES

- Glass-passivated mesa chip for reliability and uniform
- Low on-state voltage and High I_{TSM}
- RoHS products

封装 Package



序号 Pin	引线名称 Description
1	主电极 1 MT1
2	门极 G
3	主电极 2 MT2



订货信息 ORDER MESSAGES

订货型号 Order codes		印记 Marking	封装 Package
有卤-编带弹带盒装 Halogen-Tape	无卤-编带弹带盒装 Halogen-Free-Tape	3FT1T3	TO-92
3FT1T3-T-E	3FT1T3-T-ER		



绝对最大额定值 ABSOLUTE RATINGS ($T_C=25^{\circ}\text{C}$)

项 目 Parameter	符 号 Symbol	试 验 条 件 Condition	数 值 Value	单 位 Unit
重复峰值断态电压 Repetitive peak off-state voltage	V_{DRM}		± 600 ± 800	V
通态方均根电流 On-state RMS current	$I_{\text{T(RMS)}}$	full sine wave	1.0	A
非重复浪涌峰值通态电流 Non-repetitive surge peak on-state current	I_{TSM}	full sine wave , $t=20\text{ms}$	16	A
		full sine wave , $t=16.7\text{ms}$	17.6	A
	I^2t	$t=10\text{ms}$	1.28	A^2s
通态电流临界上升率 Repetitive rate of rise of on-state current after triggering	di/dt	MT1(-),MT2(+),G(+); MT1(-),MT2(+),G(-); MT1(+),MT2(-),G(-)	50	$\text{A}/\mu\text{s}$
峰值门极电流 Peak gate current	I_{GM}		2	A
平均门极功率 Average gate power	$P_{\text{G(AV)}}$		0.1	W
存储温度 Storage temperature	T_{stg}		-40~150	$^{\circ}\text{C}$
操作结温 Operation junction temperature	T_{VJ}		-40~125	$^{\circ}\text{C}$



电特性 ELECTRICAL CHARACTERISTIC (T_C=25°C)

项 目 Parameter	符 号 Symbol	测 试 条 件 Condition	最小 Min	典型 Typ	最大 Max	单位 Unit
峰值重复断态电流 Peak Repetitive Blocking Current	I _{DRM}	V _{DM} =V _{DRM} , T _j =125°C, gate open	-	-	0.5	mA
峰值通态电压 Peak on-state voltage	V _{TM}	I _{TM} =5A	-	1.4	1.7	V
门极触发电流 Gate trigger current	I _{GT}	V _{DM} =12V, R _L =100 Ω	MT1(-),MT2(+),G(+)		5	mA
			MT1(-),MT2(+),G(-)		5	mA
			MT1(+),MT2(-),G(-)		5	mA
			MT1(+),MT2(-),G(+)		45	mA
门极触发电压 Gate trigger voltage	V _{GT}	V _{DM} =12V,R _L =100 Ω	-	0.7	1.5	V
维持电流 Holding current	I _H	V _{DM} =12V, I _{GT} =0.1A			10	mA
擎住电流 Latching current	I _L	V _{DM} =12V, I _{GT} =0.1A			15	mA
断态临界电压上升率 Rise of off- state voltage	dV/dt	V _{DM} =67% V _{DRM(MAX)} , T _j =125°C, gate open	10			V/ μ s

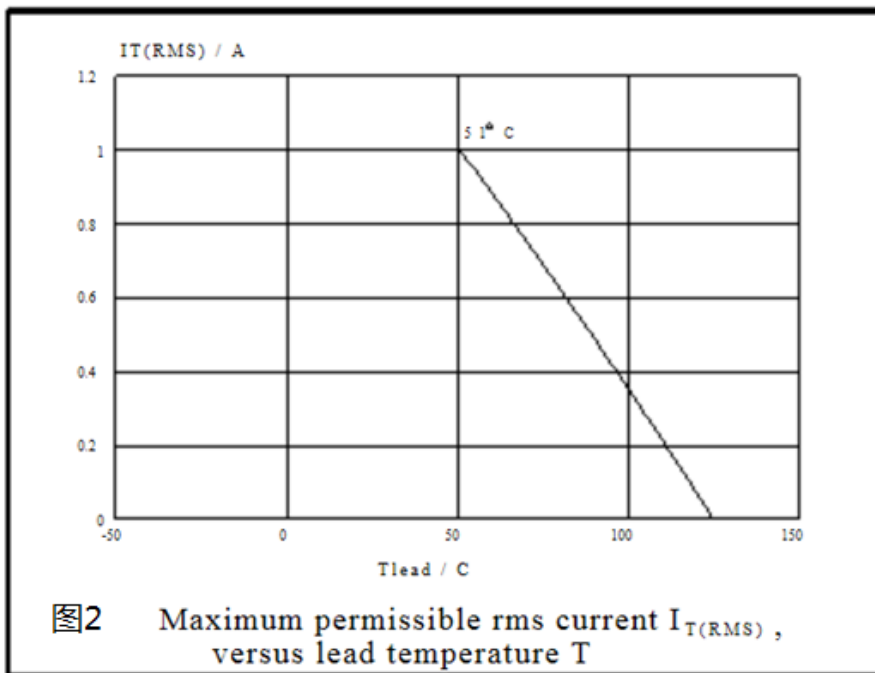
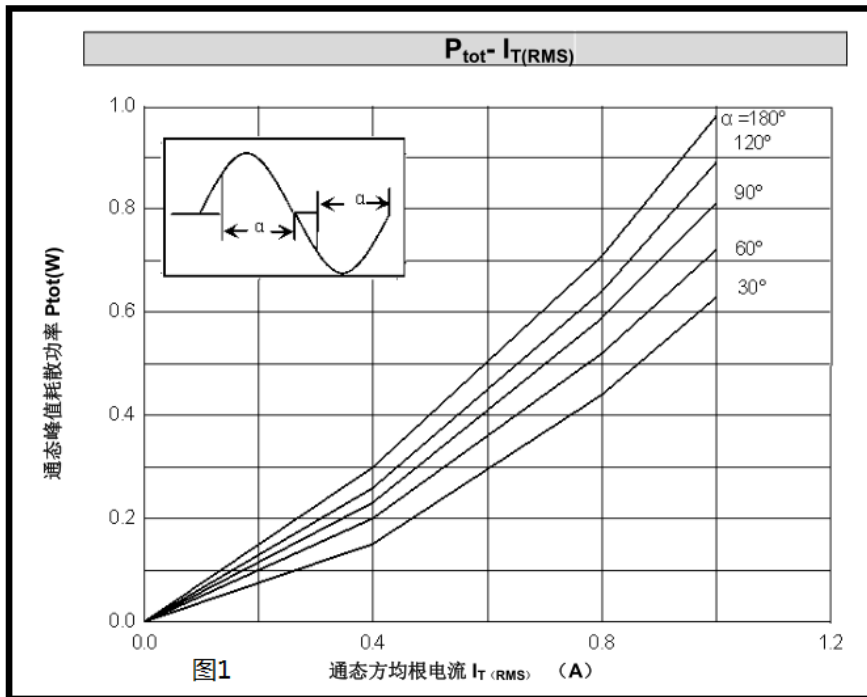
热特性 THERMAL CHARACTERISTIC

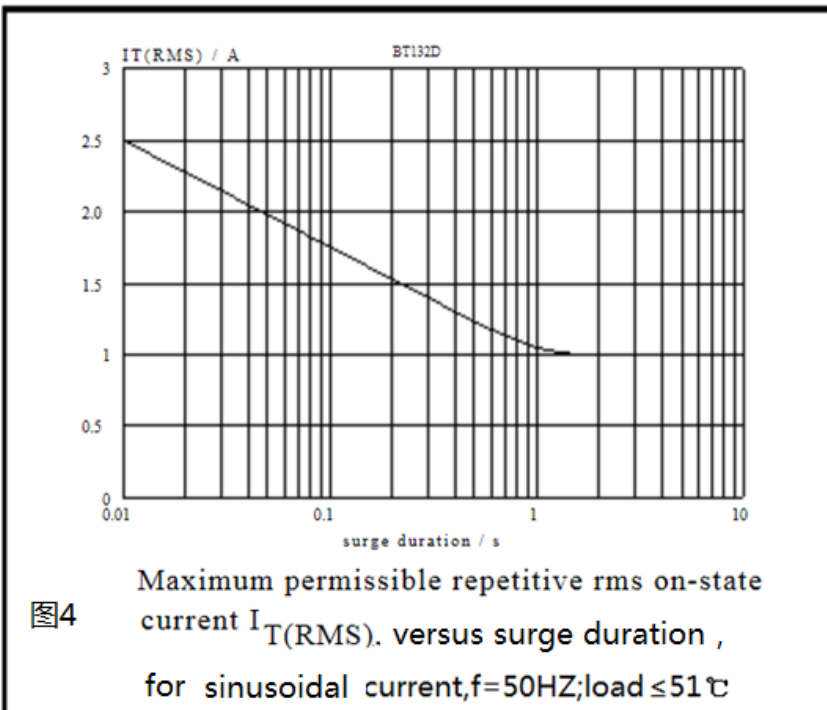
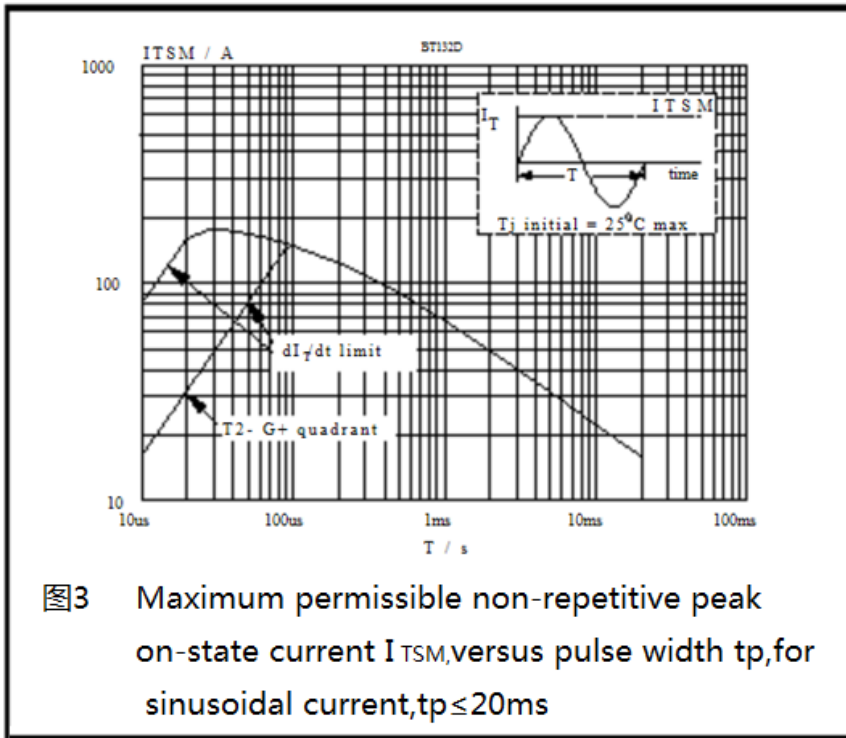
项 目 Parameter	符 号 Symbol	条 件 Condition	最小 Min	典型 Typ	最大 Max	单位 Unit
结到引线的热阻 Thermal resistance junction to lead	R _{th(j-l)}	full cycle (TO-92)	-	-	60	°C/W

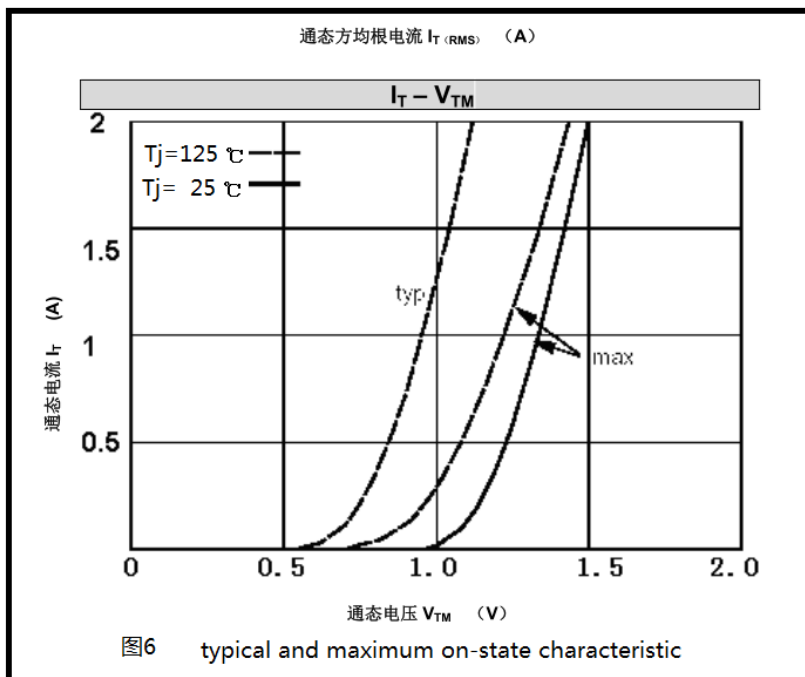
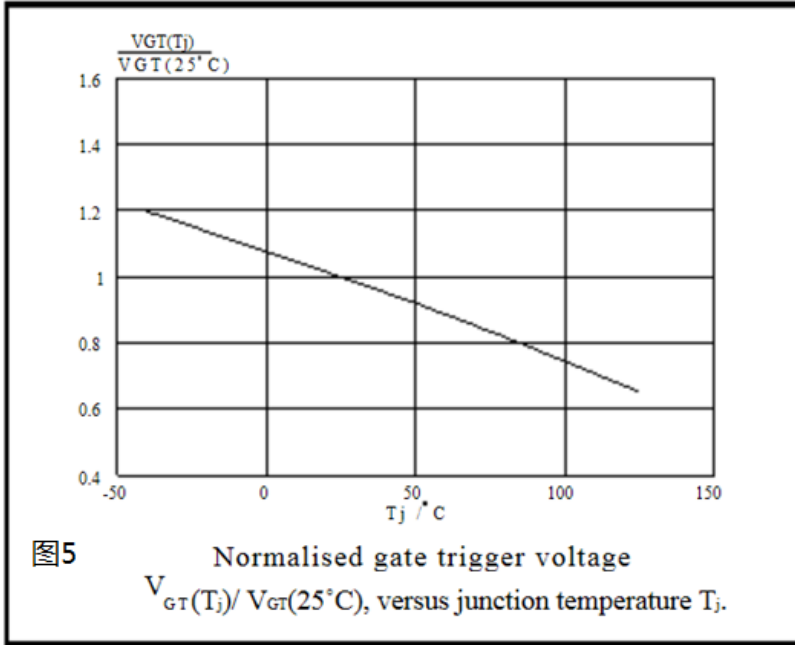




特征曲线 ELECTRICAL CHARACTERISTICS (curves)





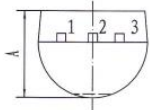
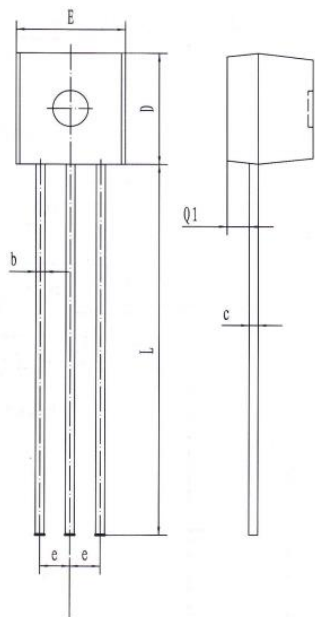




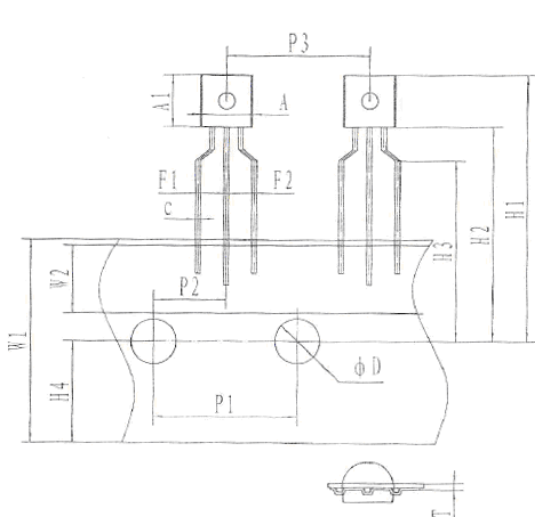
外形尺寸 PACKAGE MECHANICAL DATA

TO-92

单位 Unit : mm



符号 Symbol	尺寸 (mm) Size (mm)
A	3.30-3.90
b	0.35-0.55
c	0.31-0.51
D	4.30-4.90
E	4.30-4.90
e	1.17-1.37
L	12.50-15.50
Q1	0.85-1.00



符号 Symbol	尺寸 (mm) Size (mm)
A	4.50-4.70
A1	4.50-4.70
A2	3.50-3.70
c	TYP 0.45
F1/F2	2.20-2.80
W1	17.5-18.5
W2	5.50-6.50
H1	22.0-27.0
H2	18.0-20.0
H3	15.0-17.0
H4	8.50-9.50
P1	12.5-12.9
P2	6.00-6.70
P3	12.5-12.9
T	0.40-0.45
φ D	3.80-4.20
△H	0.00-1.00



**注意事项**

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4. 本说明书如有版本变更不另外告知

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2. We strongly recommend customers check carefully on the trademark when buying our product, if there is any question, please don't be hesitate to contact us.
3. Please do not exceed the absolute maximum ratings of the device when circuit designing.
4. Jilin Sino-microelectronics co., Ltd reserves the right to make changes in this specification sheet and is subject to change without prior notice.

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