



3DG8050

主要参数 MAIN CHARACTERISTICS

I_C	1.5A
V_{CE0}	25V
P_C	1W

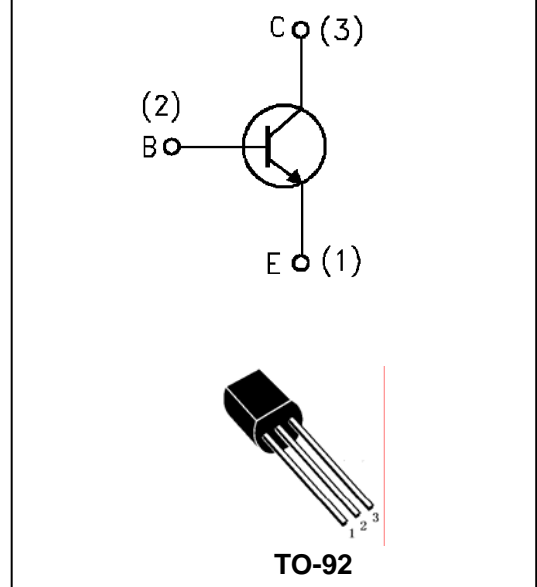
用途

- 高频开关电源
- 高频功率变换
- 一般功率放大电路

APPLICATIONS

- High frequency switching power supply
- High frequency power transform
- Commonly power amplifier circuit

封装 Package



产品特性

- 硅外延
- 高开关速度
- 与 3DG8550 互补
- 环保 (RoHS) 产品

FEATURES

- Epitaxial silicon
- High switching speed
- Complementary to 3DG8550
- RoHS product

订货信息 ORDER MESSAGE

订货型号 Order codes	印记 Marking	无卤素 Halogen Free	封装 Package	包装 Packaging
3DG8050-O-T-N-C	8050	否 NO	TO-92	袋装 Bag
3DG8050-O-T-N-A	8050	否 NO	TO-92	编带 Brede



绝对最大额定值 ABSOLUTE RATINGS (T_c=25℃)

项 目 Parameter	符 号 Symbol	数 值 Value	单 位 Unit
集电极—基极直流电压	Collector- Base Voltage (I _E =0)	V _{CB0}	40 V
集电极—发射极直流电压	Collector- Emitter Voltage (I _B =0)	V _{CEO}	25 V
发射极—基极直流电压	Emitter-Base Voltage (I _C =0)	V _{EBO}	6 V
最大集电极直流电流	Collector Current (DC)	I _C	1.5 A
最大集电极耗散功率	Total Dissipation (TO-92)	P _C	1 W
最高结温	Junction Temperature	T _j	150 °C
贮存温度	Storage Temperature	T _{stg}	-55~+150 °C

电特性 ELECTRICAL CHARACTERISTIC

项 目 Parameter	测试条件 Tests conditions	最小值 Value(min)	典型值 Value(typ)	最大值 Value(max)	单 位 Unit
V(BR) _{CBO}	I _C =100uA, I _E =0	40	-	-	V
V(BR) _{CEO}	I _C =2mA, I _B =0	25	-	-	V
V(BR) _{EBO}	I _E =100uA, I _C =0	6	-	-	V
I _{CBO}	V _{CB} =35V, I _E =0	-	-	100	nA
I _{EBO}	V _{EB} =6V, I _C =0	-	-	100	nA
Hfe(1)	V _{CE} =1V, I _C =5mA	100	-	-	
Hfe(2)	V _{CE} =1V, I _C =100mA	150	250	350	
Hfe(3)	V _{CE} =1V, I _C =800mA	80	-	-	
V _{CE(sat)}	I _C =800mA, I _B =80mA	-	0.28	0.5	V
V _{BE(sat)}	I _C =800mA, I _B =80mA	-	0.98	1.2	V
f _T	V _{CE} =10V, I _C =50mA	100	190	-	MHz

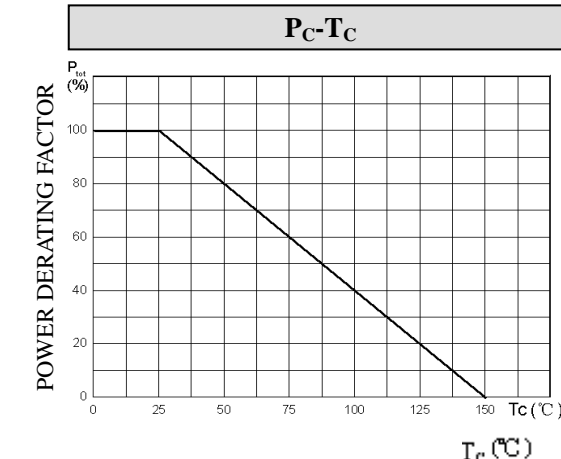
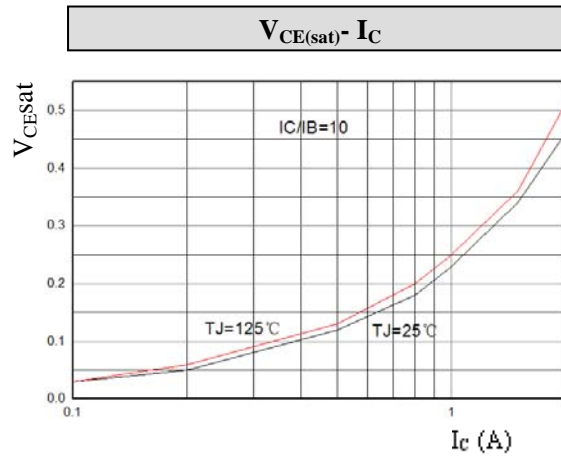
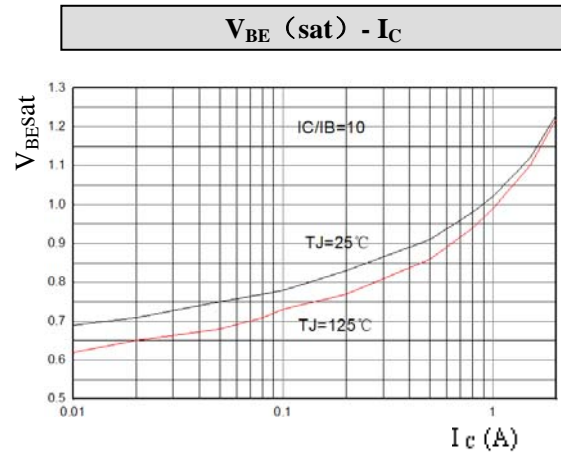
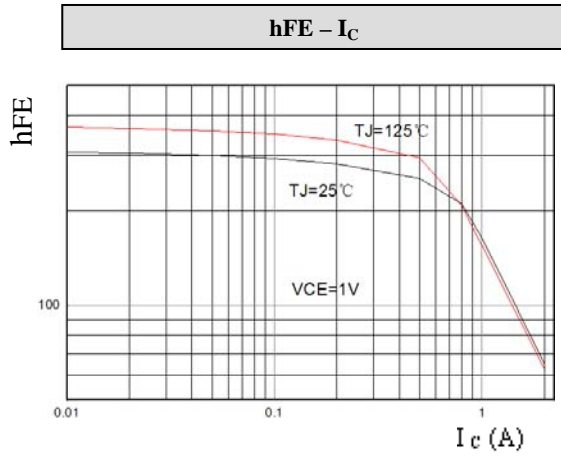
热特性 THERMAL CHARACTERISTIC

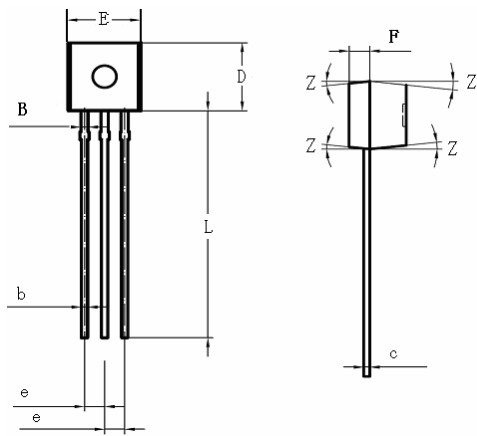
项 目 Parameter	符 号 Symbol	最小值 Value(min)	最大值 Value(max)	单 位 Unit
结到环境的热阻 TO-92 Thermal Resistance Junction Ambient TO-92	R _{th(j-a)}	-	125	°C/W



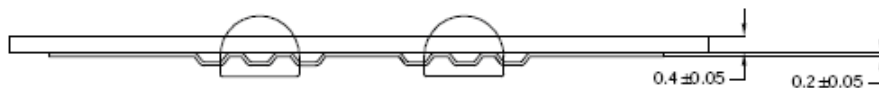
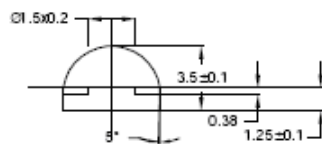
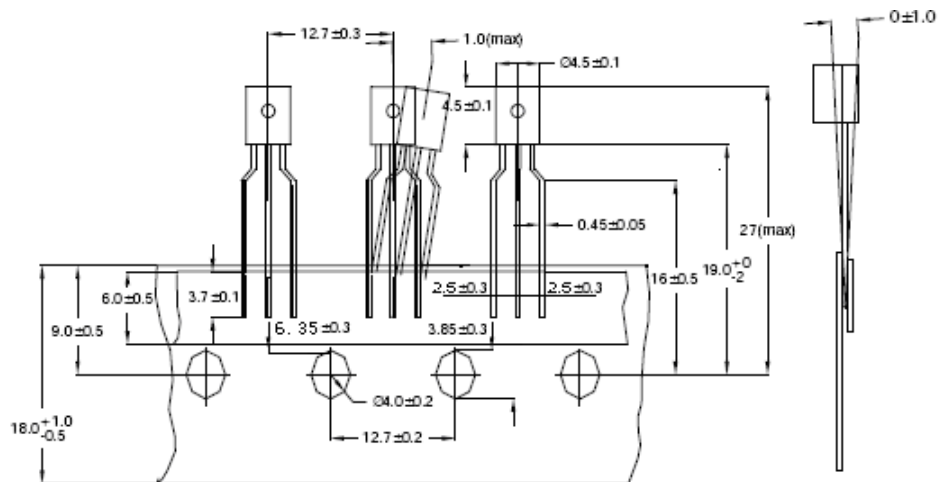
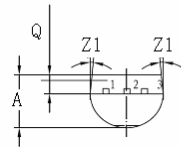


特征曲线 ELECTRICAL CHARACTERISTICS (curves)





A	3.30-3.90
B	0.35-0.55
b	0.35-0.55
c	0.31-0.51
D	4.30-4.90
E	4.30-4.90
e	1.17-1.37
F	1.00-1.60
L	12.50-15.50
Q	1.00-1.60
Z	2°
Z1	5°



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3. 在电路设计时请不要超过器件的绝对最大额定值，否则会影响整机的可靠性。
4. 本说明书如有版本变更不另外告知。

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3. Please do not exceed the absolute maximum ratings of the device when circuit designing.
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附录 (Appendix) : 修订记录 (Revision History)

日期 Date	旧版本 Last Rev.	新版本 New Rev.	修订内容 Description of Changes
2015-1-27		201501A	新版本

